

**TRIPURA UNIVERSITY**  
(A Central University)  
SURYAMANINAGAR - 799024

No. F.

**PROVISIONAL RESULT**

Date : 31-08-2026

**Master of Technology (M.Tech) in VLSI & Embedded Systems 2 SEMESTER EXAMINATION - 2026**

**Exam - Regular Even Semester-2026**

Published on - 31-08-2026

| Sl. No. | Particulars                                            | No. of Candidates |
|---------|--------------------------------------------------------|-------------------|
| 1.      | Candidate Admitted                                     | 2                 |
| 2.      | Candidate Appeared                                     | 2                 |
| 3.      | Candidate Absent                                       | 0                 |
| 4.      | Candidate Successful                                   | 0                 |
| 5.      | Candidate qualified for next semester with back papers | 0                 |
| 6.      | Candidate Failed                                       | 0                 |
| 7.      | Percentage of Candidate Passed                         | 0.00              |

This result is published with the approval of competent authority Tripura University.

Copy to:

1. The Dean, Faculty of Science /Literature/ Commerce, Law, Management and Information Science/ Social Science / Music and Fine Arts, Tripura University.
2. Director of CDC , Tripura University.
3. The Head/In-Charge .....
4. Hindi Officer, Tripura University.
5. Statistical Officer, Tripura University.
6. P.S. to the Hon'ble Vice-Chancellor, Tripura University.
7. P.A. to the Registrar, Tripura University.
8. Tripura University Notice Board/ Reception Counter/ T.U. City Centre, Agartala.
9. The website: [www.tripurauniv.ac.in](http://www.tripurauniv.ac.in)



त्रिपुरा विश्वविद्यालय, / TRIPURA UNIVERSITY,

(केंद्रीय विश्वविद्यालय / A Central University)

सूर्यमणिनगर, अगरतला, त्रिपुरा, इंडिया / Suryamaninagar, Agartala, Tripura, India, PIN - 799022

**Provisonal Result of Master of Technology (M.Tech) in VLSI & Embedded Systems 2 SEMESTER**  
**Exam - Regular Even Semester-2026**  
**RESULT DATE : 31-08-2026**

| Sr. No.                                          | Enrolment No. | Registration No. | Name          | SGPA | CGPA | Course Code(s) in which student obtained "less than P Grade" |
|--------------------------------------------------|---------------|------------------|---------------|------|------|--------------------------------------------------------------|
| 27: Tripura Institute of Technology , Narsingarh |               |                  |               |      |      |                                                              |
| 1                                                | 256704001     |                  | SAMRAT SARKAR | 8.60 | 8.46 | PASSED                                                       |
| 2                                                | 256704002     |                  | SUMIT ROY     | 9.20 | 9.00 | PASSED                                                       |

**TRIPURA UNIVERSITY**  
(A Central University)  
SURYAMANINAGAR - 799024

No. F.

**PROVISIONAL RESULT**

Date : 31-08-2026

**Master of Technology (M.Tech) in VLSI & Embedded Systems 4 SEMESTER EXAMINATION - 2026**

**Exam - Regular Even Semester-2026**

Published on - 31-08-2026

| Sl. No. | Particulars                                            | No. of Candidates |
|---------|--------------------------------------------------------|-------------------|
| 1.      | Candidate Admitted                                     | 2                 |
| 2.      | Candidate Appeared                                     | 2                 |
| 3.      | Candidate Absent                                       | 0                 |
| 4.      | Candidate Successful                                   | 0                 |
| 5.      | Candidate qualified for next semester with back papers | 0                 |
| 6.      | Candidate Failed                                       | 0                 |
| 7.      | Percentage of Candidate Passed                         | 0.00              |

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**Provisional Result of Master of Technology (M.Tech) in VLSI & Embedded Systems 4 SEMESTER**  
**Exam - Regular Even Semester-2026**  
**RESULT DATE : 31-08-2026**

| Sr. No.                                          | Enrolment No. | Registration No. | Name          | SGPA | CGPA | Course Code(s) in which student obtained "less than P Grade" |
|--------------------------------------------------|---------------|------------------|---------------|------|------|--------------------------------------------------------------|
| 27: Tripura Institute of Technology , Narsingarh |               |                  |               |      |      |                                                              |
| 1                                                | 246704001     | 2024-029519      | PAUFRU MOG    | 9.69 | 9.59 | PASSED                                                       |
| 2                                                | 246704002     | 2013-008522      | KWDWK JAMATIA | 9.81 | 9.47 | PASSED                                                       |